



Digital VLSI Chip Design with Cadence and Synopsys CAD Tools

Erik Brunvand

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KEY BENEFIT: This hands-on book leads readers through the complete process of building a ready-to-fabricate CMOS integrated circuit using popular commercial design software. **KEY TOPICS:** The VLSI CAD flow described in this book uses tools from two vendors: Cadence Design Systems, Inc. and Synopsys Inc. Detailed tutorials include step-by-step instructions and screen shots of tool windows and dialog boxes.

MARKET: A useful reference for chip designers.

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